ST. JOSEPH'S EVENING COLLEGE (AUTONOMOUS)

DEPARTMENT OF COMPUTER APPLICATIONS
TEACHING LESSON PLAN FOR COMPUTER ARCHITECTURE

BCA 5th Semester (June, 2018 to September, 2018)

Objective of the subject: To help students understand the concept of computer architecture

Name of the Faculty: Mrs. Amalraj

Time/Hours required – 60 hrs

Sl. No.	Module and Topics	No. of Hours.	Teaching methods	Evaluation of Learning process
Unit I	**Digital logic circuits** Digital IC Flip flops and registers Decoders and multiplexers Shift registers RAM AND ROM COUNTERS	**10** 2 2 2 2 2	Lecture/ACTIVITY	Exercise problems and Assignment problems
UNIT-2	**COMPUTER ORGANIZATION AND DESIGN** INSTRUCTION CODES COMPUTER INSTRUCTION TIMING AND CONTROL INPUT/OUTPUT INTERRUPT	**6** 2 1 1 1 1	Lecture/ACTIVITY	Exercise problems and Assignment problem
UNIT3	**CENTRAL PROCESSOR ORGANIZATION** processor bus organization alu stack organization	**12** 1 1 1	Lecture/ACTIVITY	Exercise problems and Assignment problems

/ **UNIT-4**	addressing modes data transfer and manipulation program control microprocessor organization	2 1 1 1 1 1 2	Lecture/ACTIVITY	Exercise problems and Assignment problems
UNIT-5	**INPUT/OUTPUT/MEMORY ORGANIZATION** peripheral devices asynchronous datatransfer direct memory access priority interrupt input/output processor auxillary memory memory hierarchy associative memory virtual memory cache memory management hardware	**20** 1 1 1 3 3 3 3 1 1 1 1 1	Lecture/ACTIVITY	Exercise problems and Assignment problems